

Class	Subclass
	1

**ISSUE CLASSIFICATION**

PATENT NUMBER

6

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## U.S. UTILITY Patent Application

SCANNED *NS 2* Q.A. *SKU*

**PATENT DATE**

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/943589	F	<del>327</del>	165	2816	P. Patel

## APPLICANTS

Rainald Sander

### III

Circuit arrangement to determine the current in a load transistor

PTO-2040  
12/99

ISSUING CLASSIFICATION										
ORIGINAL				CROSS REFERENCE(S)						
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)					
INTERNATIONAL CLASSIFICATION										

Continued on Issue Slip inside File Jacket

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)			<b>NOTICE OF ALLOWANCE MAILED</b>	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____ _____	_____ (Primary Examiner) (Date)			<b>ISSUE FEE</b>	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)			Amount Due	Date Paid
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